



Geetha Shishu Shikshana Sangha (R) GSSS INSTITUTE OF ENGINEERING & TECHNOLOGY FOR WOMEN

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Accredited Branches by NBA, New Delhi UG - ECE, CSE, ISE, EEE & TE
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KRS Road, Metagalli, Mysuru 570 016 | Karnataka | India



Conference ID # 52851



Dec
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AICTE Sponsored
5th International Conference on Electrical, Electronics, Communication, Computer Technologies
and Optimization Techniques
"ICEECCOT-2021"



Google Meet Link: [https:// meet.google.com/rwf-mbjk-fxq](https://meet.google.com/rwf-mbjk-fxq)

Time: 2:00 PM – 4:30PM

Day- 1		SESSION: 4		Session Coordinator : Ashwini K R, Asst. Prof, Dept. of EEE	
Sl. No	Paper ID	Author Name	Paper Title	Time	
1	17	Anju Singh, Shalini R, Sujata Kulkarni, Ajay Andhiwal A.V. Nirmal	Thick Film Technology based DC-DC Converter Hybrid for Space Applications	12:30 PM– 2:45 PM	
2	66	Bharathesh patel N, Dr.Manju Devi	Design of a Low Power Three-Stage Amplifier in 90nm	12:45 PM– 1:00 PM	
3	105	Shubham Chib, Gurpreet Kour Sodhi	Comparative Analysis of Random PWM Techniques for Cascaded H-Bridge Multi-Level Inverters	1:00 PM – 1:15 PM	
4	264	Prakash Kumar Singh, Kumari Nutan Singh, Jyotiska Bhardwaj	Performance evaluation of dual axis solar tracker in North-East India	1:15 PM – 1:30 PM	
5	198	Amrutha Varshini Rao, Guruswamy K. P.	Analysis, Design and Simulation of Active Front-End PWM Boost Rectifier for Battery Charging	2:30 PM – 2:45 PM	
6	209	Archita Rai, Ranjana Singh	Design and Implementation of Electric Vehicle Model with PID and ANN based controllers	2:45 PM – 3:00 PM	
7	272	Neetika Yadav, Neeta Pandey Deva Nand	CNTFET based Transmission Gate Diffusion Input Logic (C-TGDI) design	3:00 PM – 3:15 PM	

8	143	Kajal, Vijay Kumar Sharma	Reliability and PVT simulation of FinFET circuits using Cadence Virtuoso	3:15 PM – 3:30 PM
9	287	Sharadindu Roy, Siddhartha Banerjee	An Efficient Genetic Algorithm Based Multi- objective Optimization Technique for VLSI Circuit Partitioning with Reduced Power Consumption	3:30 PM – 3:45 PM
10	233	Himadri Sekhar Bhatt charyya, Aviral Yadav, Amalendu Bikash Choudhury and Chandan Kumar Chanda	Convolution Neural Network-Based SOC Estimation of Li-ion Battery in EV Applications	3:45 PM – 4:00 PM
11	261	Harshal Prajapati, Zuber Patel	High speed divide by 3/4 prescaler using NMOS like latch block	4:00 PM – 4:15 PM
12	253	Jaya Sai Deepak Vudatha, Sreenidhi Pr, Baby Sreeja	VLSI Design of Low Power 8×4 Barrel Shifter using 90 nm Pseudo NMOS technology	4:15 PM – 4:30 PM